

# Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

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Application ID:

Title of Invention:

10722792

Method of Fabricating a High-

Voltage Transistor With A Multi-

Layered Extended Drain

Structure

First Named Inventor:

**Donald Disney** 

Domestic/Foreign Application:

**Domestic Application** 

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Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention Method of Fabricating a High-Voltage Transistor With A Multi-Layered Extended Drain Structure

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10/722792

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First Named Applicant:

Donald Ray Disney

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Submitted by:	Elec. Sign.	Sign. Capacity
Bradley John Bereznak Registered Number: 33,474	ВЈВ	Attorney

Documents being submitted Files
us-ids 03692.P059DC-usidst.xml

us-ids.dtd us-ids.xsl

Comments



## **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Method of Fabricating a High-Voltage Transistor With A Multi-Layered Extended Drain Structure

Application Number:

10/722792

Confirmation Number:

7244

First Named Applicant:

**Donald Disney** Attorney Docket Number: 03692.P059DC

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Examiner:

David Nhu

Search string:

(6184555 or 6462377 or 6127703 or 5306656 or 6525372 or 6468847 or 5973360 or 5637898 or 5665994 or 4929987 or 4531173 or 6573558 or 6555873 or 6509220 or 6667213 or 6635544 or 6365932 or 4626789 or 6294818 or 5760440 or 6049108 or 5821144 or 4738936 or 5869875 or 6191447 or 4343015 or 6388286 or 6194283 or 4890144 or 6353252 or 6207994 or 5155574 or 5146298 or 4811075 or 4626879 or 4890146 or 5237193 or 5368136 or 5313082 or 5068700 or 5258636 or 20020175351 or 20020056884 or

20010015459).pn.

### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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## **US Published Applications**

Note: Applicant is not required to submit a paper copy of cited US Published Applications

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	2	20020056884	2002-05-16	Baliga	A1		
	3	20010015459	2001-08-23	Watanabe et al.	A1		

## Signature

Examiner Name	Date